

IN THE CLAIMS

Please make the following amendments to the claims:

1. (Amended) A microstrip patch array antenna having a plurality of antenna array elements on two-dimensional planar having A axis and B axis, for suppressing side lobes,

wherein the antenna array elements are linearly arranged in a direction of the A axis by spacing a first predetermined distance between the antenna array elements, the arranged array elements are arranged in a direction of the B axis by spacing a second predetermined distance between the antenna array elements, ~~and a predetermined portion of the microstrip patch array antenna having the arranged array elements are shifted in the direction of the A axis within a predetermined distance and a predetermined portion of the arranged array elements are shifted in the direction of the A axis by a third predetermined distance.~~

2. (Original) The microstrip patch array antenna as recited in claim 1, wherein A axis and B axis are perpendicular each other.

3. (Original) The microstrip patch array antenna as recited in claim 1, wherein the antenna array element is a unit radiation element.

4. (Original) The microstrip patch array antenna as recited in claim 1, wherein the antenna array element is a unit sub array element having a plurality of unit radiation elements.

5. (Amended) The microstrip patch array antenna as recited in claim 1, wherein the array elements have N integer number of antenna array elements in vertical, wherein 1 to $[\lfloor \frac{2}{N} \rfloor] \frac{N}{2}$ antenna array elements are linearly arranged in vertical direction at first

and $[\lfloor \frac{N}{2+1} \rfloor] \frac{N}{2} + 1$ to N^{th} antenna array elements are horizontally shifted in a predetermined distance based on the 1 to $[\lfloor (\frac{2}{N})^{\text{th}} \rfloor] \lfloor \frac{N}{2} \rfloor^{\text{th}}$ antenna array elements and then the $[\lfloor \frac{N}{2+1} \rfloor] \frac{N}{2} + 1$ to N^{th} antenna array elements are linearly arranged in vertical direction.

6. (Original) The microstrip patch array antenna as recited in claim 5, wherein the predetermined distance is 1/2 of distance of a space between antenna array elements.